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**CLAIMS:**

What is claimed is:

1. A method in a data processing system for indicating a power status of a plurality of devices using hierarchically encoded indicators, said method comprising the steps of:

providing a plurality of nodes, each one of said plurality of nodes including a different implementation of said plurality of devices;

associating each one of a first level of power indicators with a different one of said plurality of nodes, each one of said first level of power indicators representing one of said plurality of nodes;

associating each one of a second level of power indicators with a different one of said plurality of devices, each one of said second level of power indicators simultaneously representing a device in each one of said plurality of nodes;

indicating a power status of said plurality of nodes utilizing said first level of power indicators; and

indicating a power status of said plurality of devices utilizing said second level of power indicators.

2. The method according to claim 1, further comprising:  
said plurality of devices being a plurality of field replaceable units (FRUs).

3. The method according to claim 2, further comprising:  
determining a power status for each one of said plurality of FRUs included in a first one of said plurality of nodes;

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in response to determining that none of said plurality of FRUs included in a first one of said plurality of nodes are receiving power, deactivating said first level of power indicators; and

in response to determining that at least one of said plurality of FRUs included in a first one of said plurality of nodes is receiving power, activating said first level of power indicators.

4. The method according to claim 3, further comprising:  
determining a power status for said one of said plurality of devices in each one of said plurality of nodes; and

indicating said power status for said one of said plurality of devices in each one of said plurality of nodes utilizing one of said second level of power indicators that is associated with said one of said plurality of devices.

5. The method according to claim 4, further comprising:  
determining whether each one of said plurality of devices in each one of said plurality of nodes is receiving power;

in response to determining that none of said plurality of devices in each one of said plurality of nodes is receiving power, deactivating said one of said second level of power indicators that is associated with said one of said plurality of devices;

in response to determining that each one of said plurality of devices in each one of said plurality of nodes is receiving power, activating said one of said

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second level of power indicators that is associated with said one of said plurality of devices; and

in response to determining that at least one but not all of said plurality of devices in each one of said plurality of nodes is receiving power, partially activating said one of said second level of power indicators that is associated with said one of said plurality of devices.

6. The method according to claim 5, further comprising:  
said one of said second level of power indicators being an LED;

said step of deactivating said one of said second level of power indicators further including turning said LED off;

said step of activating said one of said second level of power indicators further including turning said LED on; and

said step of partially activating said one of said second level of power indicators further including causing said LED to blink.

7. The method according to claim 1, further comprising:  
selecting one of said plurality of nodes;  
determining a power status for each one of said plurality of devices included in said selected one of said plurality of nodes;

associating each one of said second level of power indicators with a different one of said plurality of devices included in said selected one of said plurality of nodes; and

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indicating said determined power status for each one of said plurality of devices included in said selected one of said plurality of nodes utilizing said second level of power indicators.

8. A data processing system for indicating a power status of a plurality of devices using hierarchically encoded indicators, comprising:

a plurality of nodes, each one of said plurality of nodes including a different implementation of said plurality of devices;

a first level of power indicators, each one of said first level of power indicators being associated with a different one of said plurality of nodes, each one of said first level of power indicators representing one of said plurality of nodes;

a second level of power indicators, each one of said second level of power indicators being associated with a different one of said plurality of devices, each one of said second level of power indicators simultaneously representing a device in each one of said plurality of nodes;

said first level of power indicators for indicating a power status of said plurality of nodes; and

said second level of power indicators for indicating a power status of said plurality of devices.

9. The system according to claim 8, further comprising:  
said plurality of devices being a plurality of field replaceable units (FRUs).

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10. The system according to claim 9, further comprising:  
said system including a CPU executing code for determining a power status for each one of said plurality of FRUs included in a first one of said plurality of nodes;

in response to determining that none of said plurality of FRUs included in a first one of said plurality of nodes are receiving power, said CPU executing code for deactivating said first level of power indicators; and

in response to determining that at least one of said plurality of FRUs included in a first one of said plurality of nodes is receiving power, said CPU executing code for activating said first level of power indicators.

11. The system according to claim 10, further comprising:

said CPU executing code for determining a power status for said one of said plurality of devices in each one of said plurality of nodes; and

said CPU executing code for indicating said power status for said one of said plurality of devices in each one of said plurality of nodes utilizing one of said second level of power indicators that is associated with said one of said plurality of devices.

12. The system according to claim 11, further comprising:

said CPU executing code for determining whether each one of said plurality of devices in each one of said plurality of nodes is receiving power;

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in response to determining that none of said plurality of devices in each one of said plurality of nodes is receiving power, said CPU executing code for deactivating said one of said second level of power indicators that is associated with said one of said plurality of devices;

in response to determining that each one of said plurality of devices in each one of said plurality of nodes is receiving power, said CPU executing code for activating said one of said second level of power indicators that is associated with said one of said plurality of devices; and

in response to determining that at least one but not all of said plurality of devices in each one of said plurality of nodes is receiving power, said CPU executing code for partially activating said one of said second level of power indicators that is associated with said one of said plurality of devices.

13. The system according to claim 12, further comprising:

said one of said second level of power indicators being an LED;

said CPU executing code for deactivating said one of said second level of power indicators further including turning said LED off;

said CPU executing code for activating said one of said second level of power indicators further including turning said LED on; and

said CPU executing code for partially activating said one of said second level of power indicators further including causing said LED to blink.

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14. The system according to claim 8, further comprising:  
said CPU executing code for selecting one of said plurality of nodes;

said CPU executing code for determining a power status for each one of said plurality of devices included in said selected one of said plurality of nodes;

said CPU executing code for associating each one of said second level of power indicators with a different one of said plurality of devices included in said selected one of said plurality of nodes; and

said CPU executing code for indicating said determined power status for each one of said plurality of devices included in said selected one of said plurality of nodes utilizing said second level of power indicators.

15. A computer program product for indicating a power status of a plurality of devices using hierarchically encoded indicators, said product comprising:

a plurality of nodes, each one of said plurality of nodes including a different implementation of said plurality of devices;

instruction means for associating each one of a first level of power indicators with a different one of said plurality of nodes, each one of said first level of power indicators representing one of said plurality of nodes;

instruction means for associating each one of a second level of power indicators with a different one of said plurality of devices, each one of said second level of power indicators simultaneously representing a device in each one of said plurality of nodes;

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instruction means for indicating a power status of said plurality of nodes utilizing said first level of power indicators; and

instruction means for indicating a power status of said plurality of devices utilizing said second level of power indicators.

16. The product according to claim 15, further comprising:

said plurality of devices being a plurality of field replaceable units (FRUs).

17. The product according to claim 16, further comprising:

instruction means for determining a power status for each one of said plurality of FRUs included in a first one of said plurality of nodes;

in response to determining that none of said plurality of FRUs included in a first one of said plurality of nodes are receiving power, instruction means for deactivating said first level of power indicators; and

in response to determining that at least one of said plurality of FRUs included in a first one of said plurality of nodes is receiving power, instruction means for activating said first level of power indicators.

18. The product according to claim 17, further comprising:

instruction means for determining a power status for said one of said plurality of devices in each one of said plurality of nodes; and



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instruction means for indicating said power status for said one of said plurality of devices in each one of said plurality of nodes utilizing one of said second level of power indicators that is associated with said one of said plurality of devices.

19. The product according to claim 18, further comprising:

instruction means for determining whether each one of said plurality of devices in each one of said plurality of nodes is receiving power;

in response to determining that none of said plurality of devices in each one of said plurality of nodes is receiving power, instruction means for deactivating said one of said second level of power indicators that is associated with said one of said plurality of devices;

in response to determining that each one of said plurality of devices in each one of said plurality of nodes is receiving power, instruction means for activating said one of said second level of power indicators that is associated with said one of said plurality of devices; and

in response to determining that at least one but not all of said plurality of devices in each one of said plurality of nodes is receiving power, instruction means for partially activating said one of said second level of power indicators that is associated with said one of said plurality of devices.

20. The product according to claim 19, further comprising:

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said one of said second level of power indicators being an LED;

said instruction means for deactivating said one of said second level of power indicators further including instruction means for turning said LED off;

said instruction means for activating said one of said second level of power indicators further including instruction means for turning said LED on; and

said instruction means for partially activating said one of said second level of power indicators further including instruction means for causing said LED to blink.

21. The product according to claim 15, further comprising:

instruction means for selecting one of said plurality of nodes;

instruction means for determining a power status for each one of said plurality of devices included in said selected one of said plurality of nodes;

instruction means for associating each one of said second level of power indicators with a different one of said plurality of devices included in said selected one of said plurality of nodes; and

instruction means for indicating said determined power status for each one of said plurality of devices included in said selected one of said plurality of nodes utilizing said second level of power indicators.